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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,630,627 B1
DATED : October 7, 2003
INVENTOR(S) : Tobita

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page, Item [54] and Column 1, line 1,

Title, should read: -- **MULTILAYERED WIRING SUBSTRATE WITH DUMMY WIRINGS IN PARALLEL TO SIGNAL WIRINGS** --

Title page,

Item [75], Inventor, should read

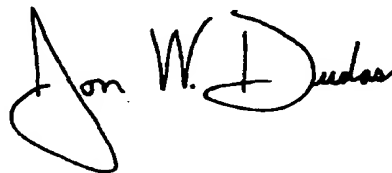
-- [75] Inventor: **Youichi Tobita, Tokyo (JP)** --

Item [73], Assignee, should read

-- [73] Assignee: **Mitsubishi Denki Kabushiki Kaisha, Tokyo (JP)** --

Signed and Sealed this

Thirtieth Day of November, 2004



JON W. DUDAS
Director of the United States Patent and Trademark Office